ROADMAP THROUGH HOSIL

FINALE

- Final Assignment
- Final Exam

AD/DA CONVERSION

- ADC/DAC: Applications, Sampling & Quantification (V)
- 3 DAC Types (S)
- DAC Example (V D)
- 4 ADC Types (S)
- ADC Example (D)
- **LAB 2** — *The participation in LAB 2 is only possible if the online course is completed until including the topic **AD/DA Conversion**.*

OPERATIONAL AMPLIFIER

- Op Amp Characteristics (V)
- Comperator (S)
- Non-Inverting Amplifier (V P S)
- Unity Gain Buffer (D)
- Inverted & Inverting Summed Amplifier (V P S)
- Active H+LP (S)
- Proof HP on basis of the equation for H+LP (P)
- **LAB 1** — *The participation in LAB 1 is only possible if the online course is completed until including the topic **Operational Amplifier**.*

SIGNALS

- Signal Characteristics (V)
- Example of determining Characteristics (D)
- Example as an application with Oscilloscope & Signal Generator (V)

INTRO

- Intro to the Course (V)
- Intro to the LAB (V D)
- LAB rules & Guidelines (D)

QnA CHANNELS & QUIZZES

QnA Channels are offered at each topic to ensure a proper as well as sustainable course mode. In addition to that various Quizzes after each module, if it may be a video, a text or equivalent, to consolidate what has been learned and to guarantee teaching quality at a high level

LEGEND

- D Document
- P Proof
- S Simulation
- V Video